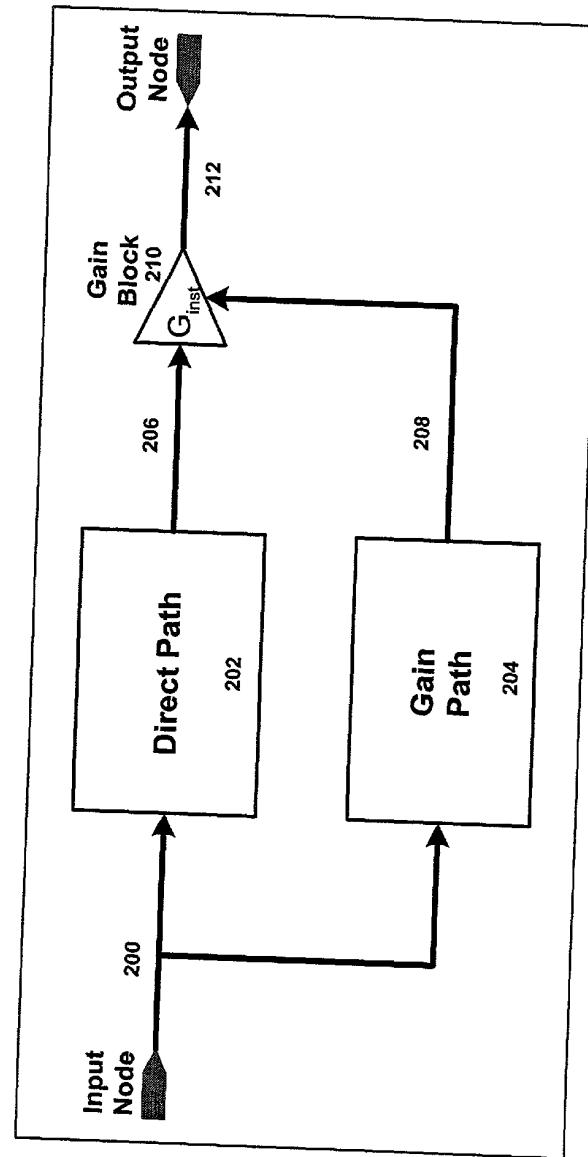


Fig. 1



**Fig. 2 - Data Flow**

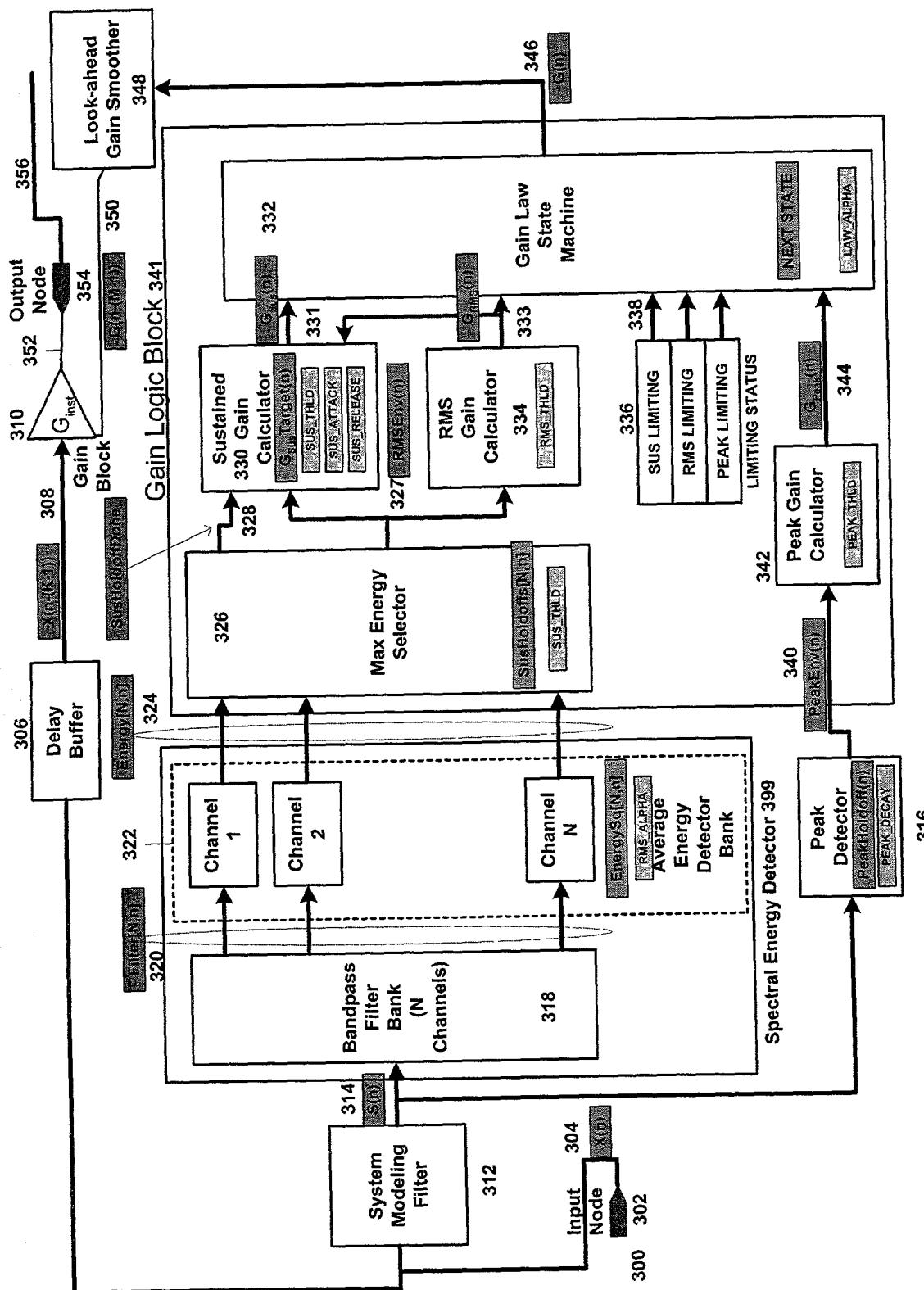


Fig. 3

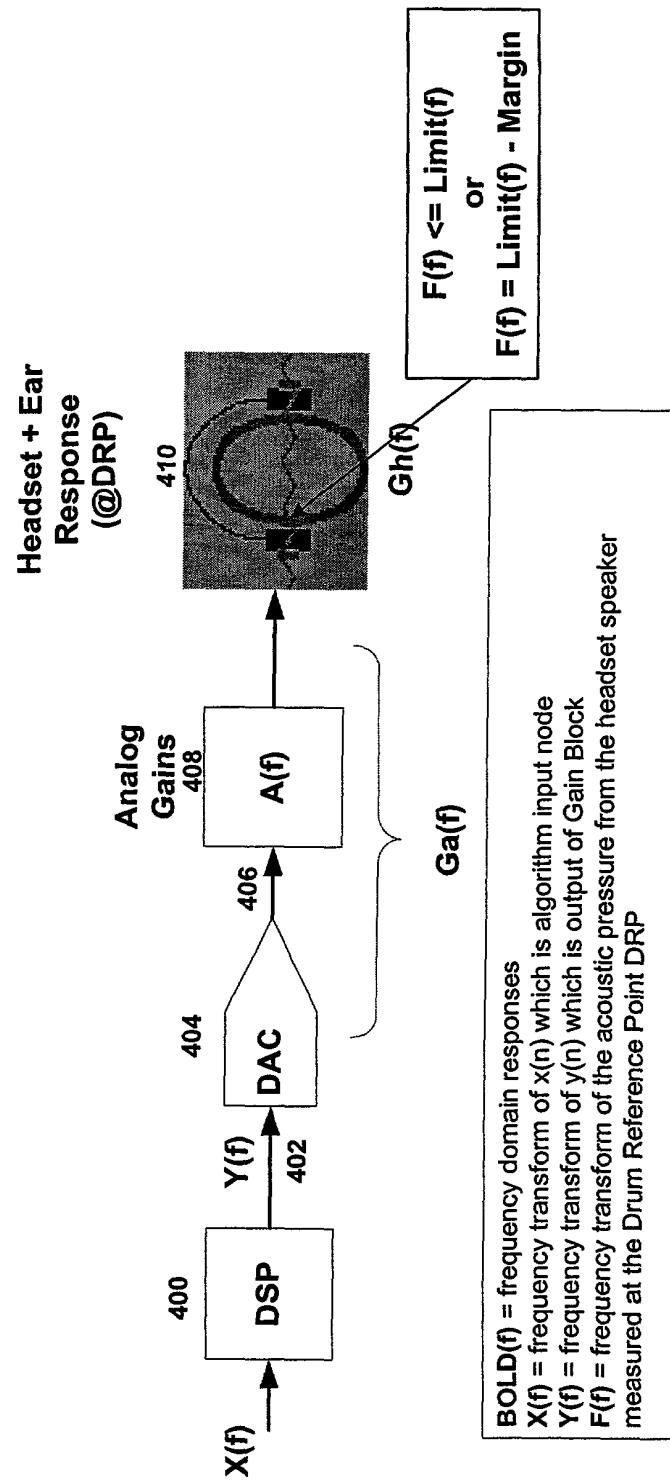
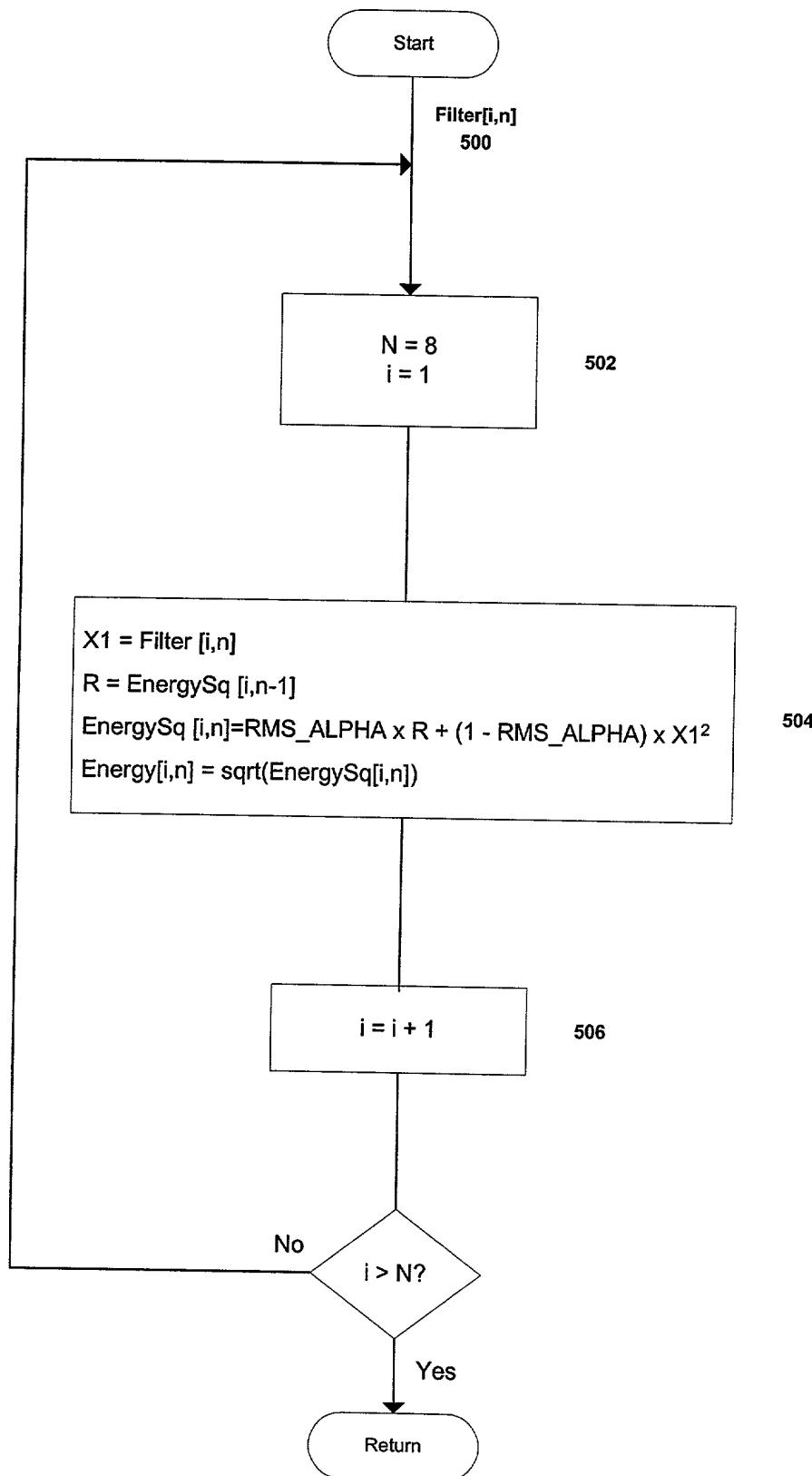
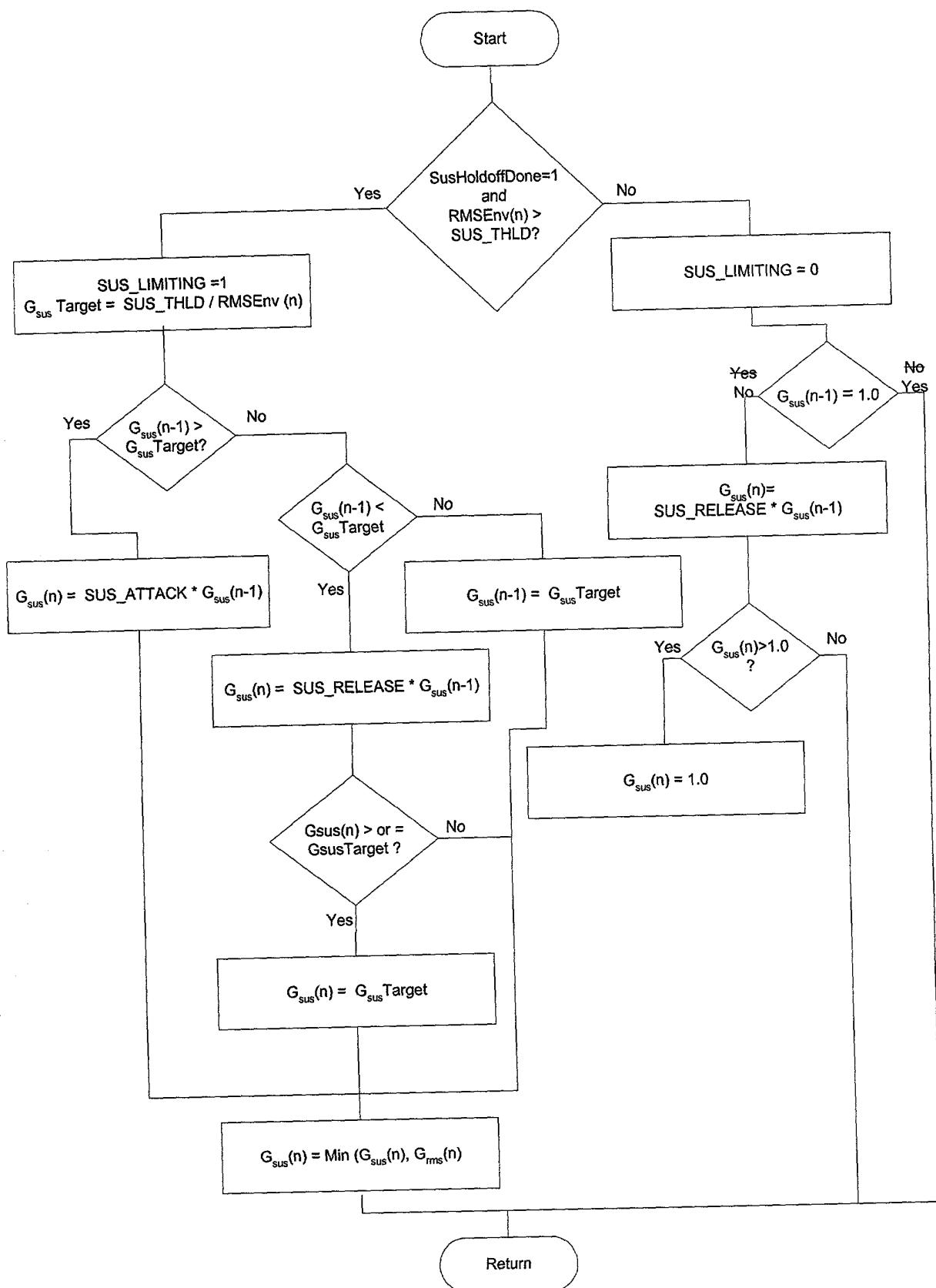


Fig. 4



**Fig. 5 - Logic for one Embodiment of Average Energy Detector**



**Fig. 6 - One Embodiment of Sustained Gain Calculator**

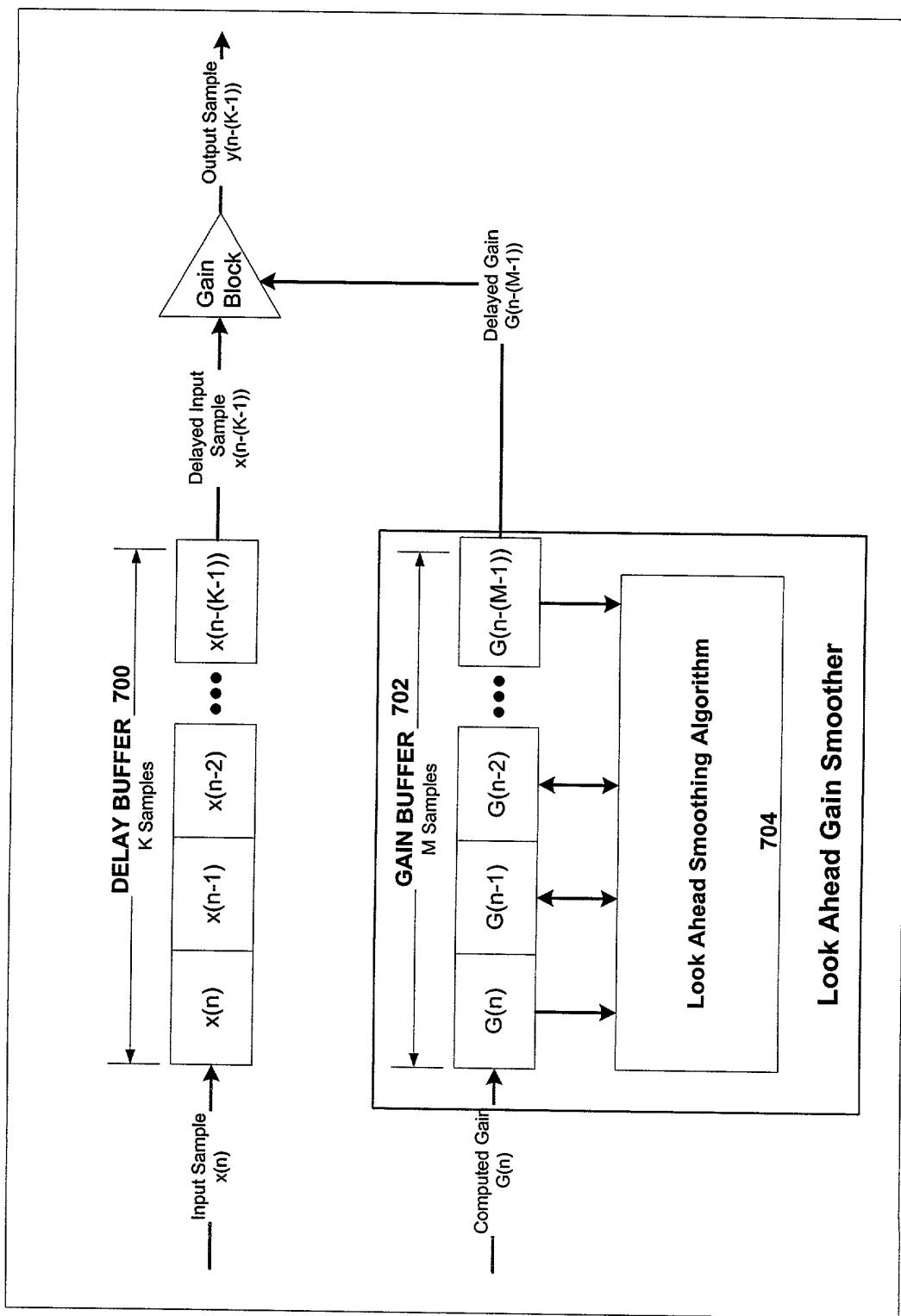


Fig. 7

### Audio limiting circuit

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Docket No.: 18864-06268

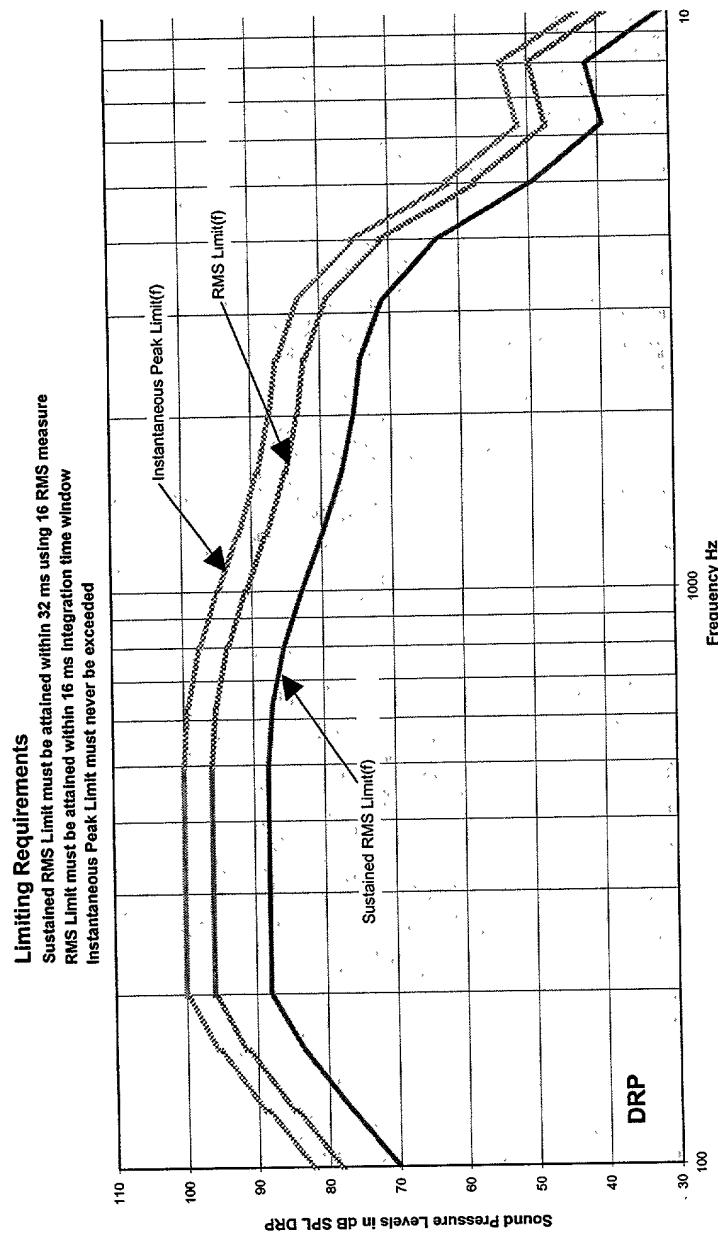
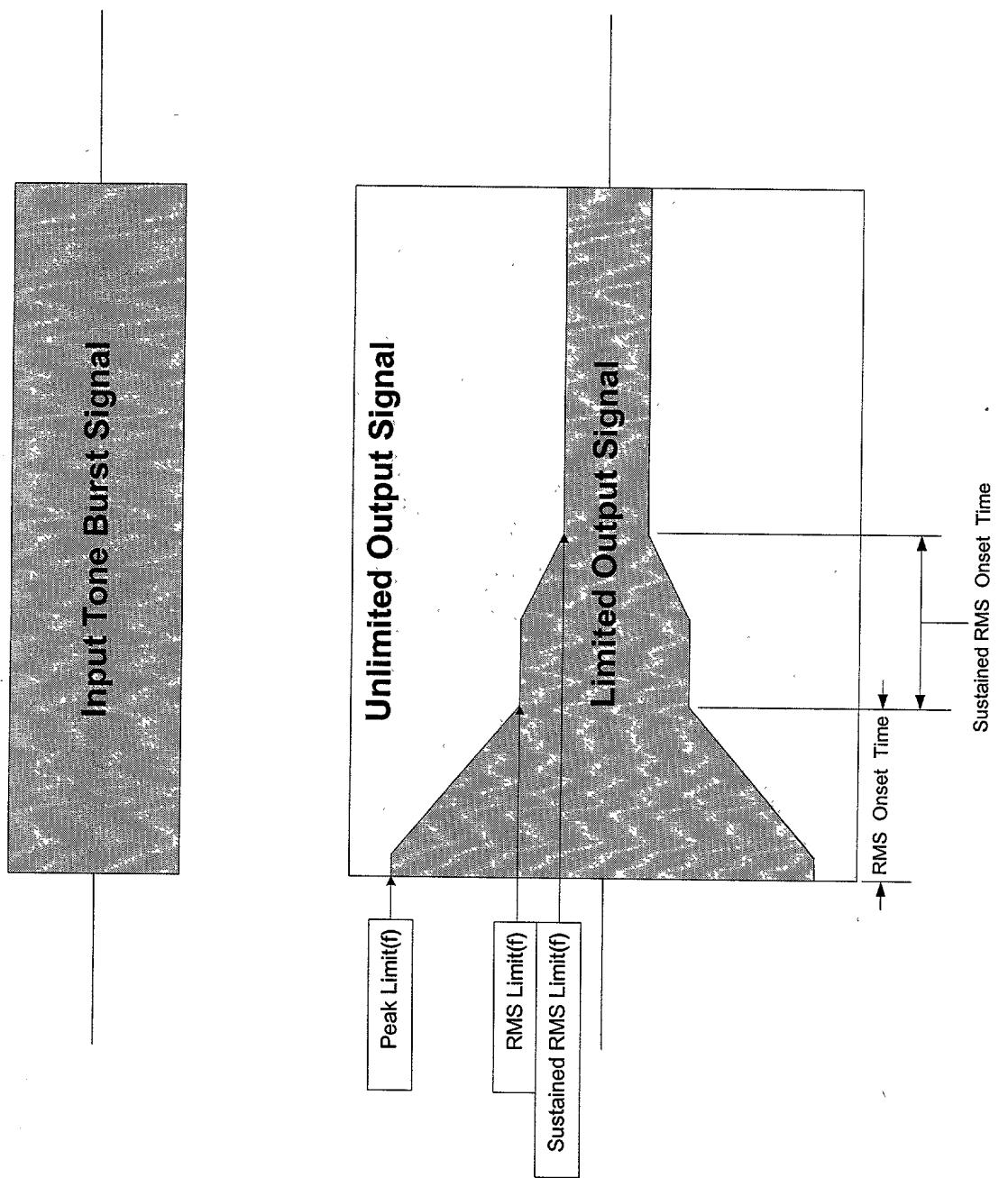


Fig. 8--Sample Frequency & Time Domain Limiting Requirements



**Fig. 9**